

Alireza Majidi

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- EDUCATION**
- Texas A&M University**, College Station, TX August 2014 - Present
PhD, Computer Science (in progress) GPA: 4.0
- University of Tehran**, Tehran, IRAN August 2009 - June 2014
Bachelor of Engineering, Computer Engineering GPA: 15/20
- SIGNIFICANT PROJECTS**
- STAPL** (Texas A&M University - Parasol Lab):
Working on the STAPL project, a framework for developing parallel C++ programs which run in both shared and distributed memory via OpenMP and MPI
- Worked on the algorithmic skeletons, the programming model of the project.
- MiniPolaris** (Texas A&M University - Advance Compiler Design Class Project):
Implemented various optimizations for a Fortran compiler as part of a class project
- Performed data and flow dependence analysis, including constant propagation
- Detected and automatically parallelized loops via OMP pragma insertion
- Identified basic blocks, and eliminated common subexpressions and dead code
- IPMACC** (Institute for Research in Fundamental Sciences - HPC Center):
An Open Source OpenACC to CUDA/OpenCL Translator
- Modeling Task Assignment & Scheduling Problem on Multi-Core Systems**
(Institute for Research in Fundamental Sciences - IPM):
- Formulated and implemented the problem using *IBM Cplex* software
- Implemented Logic-Based Benders Decomposition version of problem using *MILP* and *CP*
- NOTABLE EXPERIENCE & HONORS**
- Reviewed Computer Science Papers for Multiple Top Conferences**
- Best Paper Award at the 2015 International Conference on Supercomputing**
- Ranked 400th among 500,000 students in Iranian University Entrance Exam**
- INTERESTS** Parallel and Distributed Computing, Programming Languages & Software Engineering
- PUBLICATIONS**
- Mani Zandifar, Mustafa Abduljabbar, **Alireza Majidi**, L. Rauchwerger, N. M. Amato "Composing Algorithmic Skeletons to Express High-Performance Scientific Applications", *ICS 2015*, Newport Beach, California, **Best Paper Award Winner**
- Ahmad Lashgar, **Alireza Majidi**, Amirali Baniyasi "IPMACC: Open Source OpenACC to CUDA/OpenCL Translator", *IWOCL 2015*, Stanford University, California
- Aminollah Mahabadi, Ahmad Khonsari, Behnam Khodabandeloo, Hamid Noori, **Alireza Majidi** "Critical path-aware voltage island partitioning and floorplanning for hard real-time embedded systems", *The VLSI Journal, Integration 48*