Using Load Balancing to Scalably Parallelize Sampling-Based Motion Planning Algorithms

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Abstract—Motion planning, which is the problem of computing feasible paths in an environment for a movable object, has applications in many domains ranging from robotics, to intelligent CAD, to protein folding. The best methods for solving this PSPACE-hard problem are so-called sampling-based planners. Recent work introduced uniform spatial subdivision techniques for parallelizing sampling-based motion planning algorithms that scaled well. However, such methods are prone to load imbalance, as planning time depends on region characteristics and, for most problems, the heterogeneity of the subproblems increases as the number of processors increases. In this work, we introduce two techniques to address load imbalance in the parallelization of sampling-based motion planning algorithms; an adaptive work stealing approach and bulk-synchronous redistribution. We show that applying these techniques to representatives of the two major classes of parallel sampling-based motion planning algorithms, probabilistic roadmaps and rapidly-exploring random trees, results in a more scalable and load-balanced computation on more than 3,000 cores.

I. INTRODUCTION

While motion planning has its roots in robotics, it now finds applications in other areas of scientific computing including protein folding [30], drug design [29] and virtual prototyping and computer-aided design [8].

Due to the infeasibility of exact motion planning, sampling-based methods are now the state-of-the-art for solving motion planning problems. Sampling-based motion planning is essentially a large-scale graph problem that first constructs a graph and subsequently solves queries using graph traversals. Sampling-based motion planning algorithms have been highly successful at solving previously unsolved problems [10], and much research has focused on developing more sophisticated variants of them.

Sampling-based approaches are efficient and can be applied to problems with many degrees of freedom (e.g., robotic manipulators with many links or proteins with many amino acids). While not guaranteed to find a solution, they are known to be probabilistically complete, meaning that the probability of finding a solution, given one exists, increases with the number of samples generated [23].

Even so, substantial resources in time and hardware are still required to solve complex applications. For example, modeling the motion of a small protein using sequential sampling-based motion planning techniques can take days on a typical desktop machine [31]. Thus, it is practically infeasible to study larger proteins or to significantly increase the detail and accuracy at which their motions are modeled. Hence, there is a need for more efficient methods and parallel processing is a natural option to explore.

Previous work [19], [20] proposed methods based on uniform workspace subdivision for parallelizing representatives of the two major classes of sampling-based motion planning algorithms. By subdividing the space and restricting the locality of graph connection attempts, inter-processor communication associated with nearest neighbor searches — a well-known bottleneck in parallelizing sampling-based motion planning algorithms [2], [11], [14], [27] — can be substantially reduced. This approach achieves better and more scalable performance on different parallel machines than previous methods. Fundamentally, uniform spatial subdivision methods are limited in the types of motion planning problems they can solve efficiently. In particular, for most non-trivial environments, as the problem is subdivided, the variance in the amount of work performed by the subdivisions will increase. Because of the difference in complexity of the subdivided regions, there will be a corresponding increase in load imbalance.

In this work, we apply and analyze two techniques to address the problem of load imbalance for parallel sampling-based motion planning algorithms. The first is an adaptive work stealing approach that permanently migrates both the region and the work related to it to improve the load balance. The second approach is a bulk-synchronous redistribution technique that redistributes regions among processors to have a more balanced distribution of data. We propose a method based on the complexity of a region to approximate the amount of work per region and use it to attempt to balance work across processors, while also preserving the
spatial geometry of the subdivision.

**Contribution.** Key contributions of this paper include:

- A study of the limitations of uniform spatial subdivision for parallel sampling-based motion planning;
- Application of load balancing techniques based on graph redistribution and work stealing to combat load balancing issues that arise at scale;
- A theoretical analysis of load imbalance for a model environment and bounds on the improvement that any load balancing technique can achieve for parallel sampling-based motion planning;
- Demonstration of the performance benefit of load balancing strategies across various workloads on several parallel architectures ranging from Linux clusters to a Cray XE6 petascale machine and an experimental evaluation on more than 3,000 cores.

II. PRELIMINARIES

This section reviews basic load balancing techniques and provides an overview of previous work in parallel sampling-based motion planning.

A. Load Balancing Techniques

Work stealing [3], [4] is an important technique used to balance an imbalanced computation. In this method the computation is logically divided into a collection of tasks. When a processing element runs out of its local tasks, it attempts to steal tasks from potential victims. This strategy is well suited for shared-memory systems. In distributed-memory systems, there are two variations on the way data can be made available to the thief: replication and ownership transfer. In the case of replication, some sort of software coherence mechanism may be required to deal with the multiple copies of data. In the case of ownership transfer, the overheads associated with transferring ownership to the thief processor need to be managed. In this work, we have a model in which transfer of ownership is considered.

Repartitioning of the data is another strategy to address load imbalance. It is well known that data distribution is fundamental to achieving acceptable levels of load balance. There exists a large body of literature regarding partitioning of distributed data structures [12], [22]. We focus on computing, and enforcing through data migration, high quality partitions of the problem across processing elements.

In general, the type of load balancing technique applied to an imbalanced computation depends on the nature of the computation itself. Repartitioning is well suited for applications in which a good estimate of the computation associated with the data can be easily computed and the total amount and structure of the computation is known a priori. In contrast, work stealing is best suited for dynamic applications in which either the execution of the algorithm defines more computation as the algorithm progresses, or the work associated with the task cannot be easily estimated.

B. Parallel Sampling-Based Motion Planning

The motion planning problem is to find a valid path (e.g., one that is collision-free and satisfies any joint limit and/or loop closure constraints) for a movable object starting from a specified start configuration to a goal configuration in an environment [10]. A single configuration is specified in terms of the movable object’s $d$ independent parameters or degrees of freedom (DOF). The set of all possible configurations (both feasible and infeasible) defines a configuration space ($\mathcal{C}_{\text{space}}$). $\mathcal{C}_{\text{space}}$ is partitioned into two sets: $\mathcal{C}_{\text{free}}$ (the set of all feasible configurations) and $\mathcal{C}_{\text{obstacle}}$ (the set of all infeasible configurations). Motion planning then becomes the problem of finding a continuous sequence of points in $\mathcal{C}_{\text{free}}$ connecting the start and goal configuration.

A complete solution of the motion planning problem is considered computationally intractable and has been shown to be PSPACE-hard with the best known upper bound being doubly exponential in the movable object’s DOFs [28]. As an alternative, randomized and approximate solutions have been shown to be efficient and practical. Sampling-based methods [10] are the state-of-the-art approach to solving motion planning problems in practice. Sampling-based methods are known to be probabilistically complete, i.e., the probability of finding a solution given one exists increases with the number of samples generated. Sampling-based methods are broadly classified into two main classes: roadmap or graph-based methods such as the Probabilistic Roadmap Method (PRM) [23] and tree-based methods such as the Rapidly-exploring Random Tree (RRT) [24]. In the following, we describe previous approaches to parallelize methods from both classes.

1) Parallelizing PRM with Uniform Subdivision: The Probabilistic Roadmap Method (PRM) [23] constructs a graph $G = (V, E)$, called a roadmap, to capture the connectivity of $\mathcal{C}_{\text{free}}$. A node in $G$ represents a valid configuration and an edge represents a valid trajectory (path) between configurations. Nodes are generated using some sampling strategy and connections are attempted between a node and its $k$-nearest neighbors as computed using some distance metric. Once the roadmap is constructed, query processing is done by connecting the start and goal configurations to the roadmap and extracting a path through the roadmap that connects them.

A uniform $\mathcal{C}_{\text{space}}$ subdivision method for parallelizing PRM was presented in [19] and is shown in Algorithm 1. In line 2 of Algorithm 1, the $\mathcal{C}_{\text{space}}$ representing the movable object is subdivided into regions. For example, in three-dimensional environments, the planning space may be subdivided into regions using the $\mathcal{C}_{\text{space}}$ positional degrees of freedom, i.e., the $x$, $y$ and $z$ dimensions. A simple illustration of a 2D environment subdivided into four regions is shown in Figure 1(a). The subdivision is represented by a region graph, whose vertices represent regions and whose
edges encode the adjacency information between regions. Figure 1(b) shows the region graph corresponding to the subdivision shown in Figure 1(a).

**Algorithm 1 Uniform Subdivision**

**Input:** An environment $env$, the number of nodes $N$, the number of processes $p$, the number of regions $N_r$.

**Output:** A roadmap graph $G$.

1. Let region graph $R(V,E) = \emptyset$.
2. Let $R_d = \text{Subdivide} env$ into $N_r$ regions.
3. Add a vertex for each region $r$ of $R_d$ to $R$.
4. for all neighboring regions $(r_1, r_2) \in R_d$ par do
5. Add the edge $(r_1, r_2)$ to $R$.
6. end for
7. for all regions $r_i \in V$ par do
8. $G \leftarrow$ Independently construct regional roadmap using sequential PRM
9. end for
10. for all neighboring regions $(r_i, r_j) \in E$ par do
11. $G \leftarrow$ Connect regional roadmap of regions $r_i$ and $r_j$
12. end for
13. return $G$

**Figure 1.** A 2D environment subdivided into 4 regions and the corresponding region graph.

In line 8 of Algorithm 1, roadmaps are constructed in parallel in each region. This is done by invoking the (sequential) PRM planner [23] in each region. Lastly, in lines 10 – 12, the regional roadmaps are connected to form a roadmap of the entire $C_{free}$. The region graph facilitates the process for connecting regional roadmaps by identifying adjacent regions between which connections are attempted. Some user-defined overlap is allowed between regions to allow sampling in the portion of space at the boundaries that may facilitate connections between regional roadmaps.

2) **Parallelizing RRT with Uniform Radial Subdivision:** The Rapidly-exploring Random Tree (RRT) method is another sampling-based motion planning approach particularly well suited for non-holonomic and kinodynamic motion planning problems [24]. The basic sequential RRT grows a tree rooted at the start configuration that expands outward into unexplored areas of $C_{space}$. To build a tree, RRT first generates a uniform random sample $q_{rand}$, and identifies the closest node $q_{near}$ in the tree to $q_{rand}$, and then $q_{near}$ is “extended” toward $q_{rand}$ for a stepsize of at most $\Delta q$. If successful, $q_{new}$ is added to the tree as a node and the pair $(q_{near}, q_{new})$ is added as an edge. To solve a particular motion planning query, RRT repeats this process until the goal configuration can be connected to the tree.

Uniform radial subdivision [20] is particularly suited for parallelizing RRTs. In their method, $C_{space}$ is subdivided into conical regions and subtrees are built in each region using the sequential RRT planners. Similar to uniform subdivision described earlier, regional subtrees are later connected to subtrees in neighboring regions. A 2D illustration of radial subdivision of $C_{space}$ is shown in Figure 2.

![Figure 2. Example of uniform radial subdivision for a 2D $C_{space}$. Each process concurrently builds a branch (using sequential RRT) rooted at $q_{root}$ and biased toward a target (e.g., $q_k$ for the blue process).](image)

The uniform radial subdivision algorithm is shown in Algorithm 2. Lines 1 and 2 describe the region construction phase. First, a hypersphere is created in $d$-dimensional $C_{space} S^d$ centered at $q_{root} \in R^d$ with radius $r$. Next, $N_r$ points $q_i \in R^d$ are sampled on the surface of $S^d$. Each point defines a conical region centered around the ray $q_{root}q_i$. A region graph $G(V,E)$ is then constructed. Each vertex $v_i \in V$ represents a region defined by $q_i$ and an edge $(v_i, v_j)$ is added if $q_j$ is in the $k$ closest neighbors of $q_i$.

After region graph construction, independently (in parallel) a sequential RRT is used in each region. The subtree growth in each region is biased toward the region candidate defined by the random ray $q_{root}q_i$. Some overlap between regions is allowed so branches can explore part of the space in adjacent regions. Lastly, using the adjacency information provided by the region graph, connection attempts are made between each region branch and the branches in adjacent regions. If any edge connection creates a cycle, the tree is pruned so as to remove the cycle.

**III. LOAD BALANCING FOR PARALLEL MOTION PLANNING**

Uniform (radial) subdivision is limited in the types of motion planning environments it can handle. It performs well in uniform and homogeneous environments, but not so
processors are assigned to each region, subdivision of the planning space in Figure 3(a); if different to load imbalance. As an illustration, consider the uniform doorway, etc. Uniform subdivision in this scenario is prone logically separate parts; open or free space, cluttered space, example, a house or factory floor is typically composed of well in non-uniform and heterogeneous environments. For example, a house or factory floor is typically composed of logically separate parts; open or free space, cluttered space, doorways, etc. Uniform subdivision in this scenario is prone to load imbalance. As an illustration, consider the uniform subdivision of the planning space in Figure 3(a); if different processors are assigned to each region, processors assigned to region \( R_0 \) are relatively overloaded. This irregularity in planning space leads to workload imbalance, which will have an overall negative affect on scalability.

Figure 3 illustrates the roadmap distribution for an environment that suffers from a high degree of load imbalance using uniform spatial subdivision. Shown is a sample run with four processors where the color of a node represents a single processor. Figure 3(b) shows that the majority of the roadmap nodes are only present on two processors, and the remaining two processors have only a small number of vertices. In contrast, Figure 3(c) shows an even distribution of roadmap nodes after applying load balancing techniques.

One important consideration is the granularity in which the problem is partitioned, as the size of the biggest quanta of work establishes a lower bound by which the problem can be balanced using any load balancing strategy. In addition, a more refined problem provides more opportunity to distribute work amongst processing elements. For parallel motion planning, regions represent the quanta of work and thus for the presented load balancing strategies, we consider an over-partitioned region graph.

In general, a load balancing strategy using repartitioning requires a reasonable estimate for the amount of effort that is required to compute a quanta of work. In section III-B, we will discuss weighting techniques for the two discussed parallel motion planning algorithms and the difficulty of estimating work for uniform radial subdivision RRT.

A. Work Stealing for Parallel Sampling-Based Planning

Work stealing [4] is a well-known load balancing technique that does not require an estimate of the cost of a quanta of work. In parallel sampling-based motion planning, work stealing can be applied to either the uniform subdivision PRM or uniform radial subdivision RRT algorithms.

Algorithm 3 Work stealing Parallel Sampling-Based Motion Planning

**Input:** Region graph, sequential planner, steal policy.

**Output:** Set of constructed RRT branches or PRM roadmaps

```latex
\begin{algorithm}
\caption{Work stealing Parallel Sampling-Based Motion Planning}
\begin{algorithmic}
\State \textbf{Input:} Region graph, sequential planner, steal policy.
\State \textbf{Output:} Set of constructed RRT branches or PRM roadmaps
\State \textbf{1:} \textbf{while} Global termination not detected \textbf{do}
\State \textbf{2:} \hspace{1em} \textbf{for} \textbf{all} \( p \in \text{Processors} \) \textbf{par do}
\State \textbf{3:} \hspace{2em} \( Q \leftarrow \{ \text{Regions of } p \} \)
\State \textbf{4:} \hspace{2em} \textbf{while} \( Q \) is not empty \textbf{do}
\State \textbf{5:} \hspace{3em} \( R_{current} \leftarrow \text{DEQUEUE}(Q) \)
\State \textbf{6:} \hspace{3em} \( T \leftarrow \text{Independently construct regional subtree or roadmap using sequential planners using } R_{current} \)
\State \textbf{7:} \hspace{2em} \textbf{end while}
\State \textbf{8:} \hspace{1em} \( V \leftarrow \text{choose victim based on steal-policy} \)
\State \textbf{9:} \hspace{1em} \text{Steal regions from } V \text{ based on policy}
\State \textbf{10:} \hspace{1em} \textbf{end for}
\State \textbf{11:} \hspace{1em} \textbf{end while}
\end{algorithmic}
\end{algorithm}
```

Algorithm 3 shows a generic work stealing approach for parallel sampling-based motion planning, highlighting
opportunities to steal during either the construction of RRTs for uniform radial subdivision or a roadmap for PRM. The main computation in which the (sequential) planner is invoked is shown in Line 6. As each processor is assigned regions in which to explore, we model these regions in a local work queue. When this local queue is depleted, the processing element will issue steal requests to potential victims. On a victim processor, work is stolen from the back of its local work queue.

Victim selection is a particularly important decision, primarily because the cost of stealing from a processor on the same shared-memory node is generally less than the cost of stealing from a processor on another node. Additionally, for parallel motion planning, the choice of victims should also be related to the distribution of the region graph among the processors. In particular, since neighboring processing elements will communicate with each other to perform region connections after the RRT expansion or PRM construction phase, stealing from neighbors would benefit the region connection phase because connecting regions will be local to the same processing element.

We consider three work stealing strategies in the context of parallel motion planning. One strategy (RAND-K) is a randomized strategy in which a thief requests additional regions from \( k \) random processors, but not necessarily the same \( k \) processors for each request. For the purpose of our experimental evaluation, we have fixed \( k \) to be 8. Another strategy is DIFFUSIVE wherein processors are assumed to be arranged in a 2D mesh and underloaded processors will request neighboring processors for work. Finally, we consider the HYBRID strategy wherein we first execute DIFFUSIVE stealing and in the event that no request could be serviced, requests are sent to random processors. In the experimental results section, we compare and contrast these strategies.

### B. Repartitioning for Parallel Sampling-Based Planning

In Algorithm 4, we show how to use repartitioning to influence load balancing in parallel PRM and RRT. The main imbalanced computation, construction of independent roadmaps or RRTs for a given region, is performed only after attempting to redistribute the region graph based on the weight for each region. This will balance this phase of computation according to a suitable estimate of work to compute a region. Note that lines 5-7 in the algorithm can be used in lines 7-9 in the original parallel PRM algorithm (Algorithm 1), and we simply redistribute the regions before constructing individual roadmaps. Similarly, lines 5-7 in the algorithm can be plugged into lines 10-12 in the uniform radial subdivision RRT algorithm (Algorithm 2), provided a suitable weight for a region can be computed.

The effectiveness of repartitioning is highly dependent on the ability to estimate the load of an RRT or PRM region.

**Algorithm 4 Repartitioning for Parallel Sampling-Based Motion Planning**

**Input:** Regional graph, sequential planner

**Output:** Set of constructed RRT branches or PRM roadmaps

1: \( W \leftarrow \emptyset \)
2: for all regions \( v_i \in V \) par do
3: \( W_i \leftarrow \text{ComputeRegionWeight}(v_i) \)
4: end for
5: GraphRepartition\((R, W)\)
6: for all regions \( v_i \in V \) par do
7: \( G \leftarrow \text{Independently construct regional roadmap or RRT using sequential planners} \)
8: end for

**PRM.** For uniform subdivision PRM, since we can easily and cheaply compute a cost metric of the amount of work to be done, this algorithm is a good candidate to use repartitioning. In parallelizing PRM, the two data structures of interest are the graph representation of the regions and the PRM roadmap. The regions represent a spatial subdivision of the environment in which configurations will be sampled. Connections are attempted between configurations through the use of local planning methods. It is well known in motion planning that the cost of connecting samples in \( C_{\text{space}} \) is highly representative of the amount of time the overall algorithm will take in generating a solution [31]. This in fact is the most time consuming phase of the entire computation. As regions that have a high number of samples will generally incur a large number of local planning calls, a good metric for approximating the amount of work that a region will generate is the number of samples in the roadmap that lie within that region.

Using this information, we can determine that load imbalance in terms of regions corresponds to the number of roadmap samples of the region, and this metric can be used to weight regions. A high quality partition of the region graph will attempt to balance the regions based on this metric. However, as regions are also spatial entities, the spatial geometry of regions should also be preserved in an ideal partition. By partitioning the region graph using these approximations of the amount of work that a region will perform, the algorithm will see a higher level of load balance for subsequent phases of computation.

**RRT.** Unfortunately, in uniform radial subdivision RRT, the amount of work a region will perform is difficult to estimate beforehand. In our experiments, we show an estimate of work for an RRT branch that uses \( k \) random rays originating from the origin of the region, and computes the minimum distance to an obstacle in the direction of these rays. Intuitively, this should give a reasonable approximation of the amount of reachable free space in that region; however, we show that this metric is a poor indicator of
work for a given region unless a large number of rays is utilized, making this an expensive operation to calculate.

IV. EXPERIMENTAL EVALUATION

We implement and evaluate standard load balancing techniques for parallel motion planning and show that with an appropriate estimate for the amount of work in a region, geometric repartitioning outperforms work stealing. In the dynamic case where load is unknown a priori, repartitioning will be at a disadvantage and can potentially be worse than performing no load balancing at all.

Experimental studies were conducted on two massively parallel machines: a 153,216 core Cray XE6 (HOPPER) and a 2,400 core Opteron cluster (OPTERON-CLUSTER). Unless otherwise noted, all experiments show strong scaling in which the total number of regions is kept constant as the number of processing elements increases.

A. Implementation in STAPL

The parallel motion planning algorithms and load balancing techniques described have been implemented using STAPL [5], a generic, scalable framework for parallel C++ code development. At STAPL’s core is a library of C++ components implementing parallel algorithms (pAlgorithms) and distributed data structures (pContainers) that have interfaces similar to the (sequential) C++ standard template library (STL) [25]. Parallel algorithms are expressed as arbitrary task dependence graphs in STAPL.

Load imbalance in parallel computations is dealt with in various ways in STAPL. For repartitioning, this is realized through redistribution of the two pGraphs [16] (i.e., the region graph and the roadmap or RRT graph) in the parallel motion planning algorithms. Alternatively, load balancing can be addressed by using a custom work stealing scheduler for parallel motion planning algorithms.

B. Model PRM Environment

Consider a 2D environment with a single square obstacle that lies equidistant from the bounding box. It is possible to compute the volume of the free space ($V_{free}$) by using the total volume of the region and the volume of the obstacle within the region. With an estimate of the free space in the environment, we can say that the total load that the region will experience is proportional to $V_{free}$.

One measure of imbalance among processors is the coefficient of variation, defined to be the ratio of the standard deviation $\sigma$ and mean $\mu$ load. A naïve mapping of regions to processors would perform a 1D partitioning of the region mesh and assign a balanced number of region columns to processors. This naïve region mapping will have a high coefficient of variation for the model environment. We find an estimate of the most balanced partitioning of the region graph statically ignoring edge-cuts using a greedy global partitioning algorithm, as the exact problem is NP-complete.

Figure 4(a) shows the model’s prediction of the imbalance in terms of the coefficient of variation of samples (lower is better) with the naïve partitioning strategy and the best load balance possible. In addition, we plot the measure of load imbalance experienced during a trial run of the algorithm using repartitioning and show that we closely track the model. As shown, the best possible distribution of regions to processors for higher core counts shows less benefit, as each processor has an increasingly smaller granularity of work as the number of processors increases.

Figure 4(b) studies various metrics according to the model and an experimental evaluation. We study the potential improvement according to the model (theoretical), which measures the total reduction in $V_{free}$ for the processor with the highest amount of $V_{free}$. Next, we plot the reduction in the number of roadmap nodes (experimental) on the highest loaded processor. Finally, we show the overall improvement in execution time (runtime) for the load-balanced phase using repartitioning. In general, we track the model’s theoretical estimate of the best load distribution in terms of roadmap nodes, which in turn tracks the improvement in execution time. The discrepancies between the best distribution of $V_{free}$ and the roadmap node distribution can be explained by both the probabilistic nature of the computation and by
the geometric restrictions enforced by the repartitioning. The gap between the improvement in roadmap distribution and total time reduction is a result of the number of roadmap nodes per region being an imperfect indicator of the total amount of work generated by that region. At 128 cores, there is no better distribution of load possible, so the experimental result only shows the overhead of attempting to repartition.

C. Experimental Results

1) Repartitioning: PRM. The environments considered in this section are variants of a 3D narrow passage with a rigid-body robot, similar to the theoretical environment that consists of a single cubic obstacle in which roughly 24% (med-cube), 6% (small-cube) and 0% (free) of the environment is blocked. In all environments, we subdivide the problem into 250,000 regions total. Figure 5(a) shows raw execution time for computing the final roadmap on the HOPPER platform for this strong scaling PRM experiment in the med-cube environment. We can see that using repartitioning, we are able to achieve a 2.9x improvement over the baseline on 96 cores and a 1.68x improvement on 768 cores. Because of the strong scaling nature of our experiment, there are significantly fewer regions per processor at 768 cores, which allows for less opportunity for moving load across processors. From Figure 5(b), we can see that although the coefficient of variation is substantially lower for all processor counts after repartitioning, the difference is not as much for higher processor counts simply because of a reduced opportunity to rebalance. Figure 5(c) shows the distribution of load across processors on a 192-core run on HOPPER. We see that without load balancing, there is a wide spread in work and after applying repartitioning, a distribution closer to the ideal is achieved. Figure 6 shows the trend shown in the previous analysis holds for higher processor counts on HOPPER.

For the same experiment, we show the breakdown of the various phases of parallel PRM in Figure 7(a). As suspected, the portion of the computation connecting roadmap nodes in a region (line 8 in Algorithm 1) dominates most of the computation at 90% of the total execution time. After load balancing for both methods, the total time decreases, mainly because of the decrease in node connection time. For repartitioning, there is an increase in region connection time (line 11 in Algorithm 1), which can be partially attributed to an increase in remote accesses in the region connection phase, as shown in Figure 7(b). This is due to an increase in edge cuts, which was induced by repartitioning.

Figure 8 demonstrates load balancing techniques on multiple environments that display different levels of imbalance on OPTERON-CLUSTER. In Figure 8(a) and (b), we see up to a 3.4x speedup over the baseline using repartitioning in the med-cube environment, but only a 1.2x speedup in the small-cube environment. This shows that even on workloads that are not imbalanced to such a high degree, load balancing can still provide a large benefit. In addition, we find that in the free environment which exhibits no imbalance, all load balancing techniques do not show any significant overhead over the non-load balanced variant, as shown in Figure 8(c).

RRT. We also evaluated load balancing techniques on the uniform radial RRT parallel motion planning algorithm. As discussed in Section III-B, it is difficult to estimate the amount of work that a radial branch will compute due to the probabilistic and dynamic nature of the algorithm. Thus, computing an effective partition for load balancing is difficult and may prove to be inaccurate.

In Figure 10(b), we computed an estimate of work for an RRT branch by using the $k$ random rays metric discussed
in Section III-B. This metric should intuitively estimate the number of local planning calls in the RRT construction, yet it acts as a poor estimate for the amount of work needed to compute the RRT branch and in most cases, we see a slowdown when using this weight. Indeed, any metric to estimate the amount of work in this random and dynamic algorithm would likely be imprecise and it is for this reason work stealing strategies are better suited for uniform subdivision radial RRT.

2) Work Stealing: PRM. In the PRM environments, we see that HYBRID work stealing outperforms the RAND-K strategy, mainly due to the non-deterministic behavior of RAND-K and the low probability of finding work using a random strategy. Figure 9 provides a breakdown for HYBRID work stealing illustrating the number of tasks that were executed locally and the number of stolen tasks for each processor. In Figure 9(a), we see that a substantial number of underloaded processors find work to be stolen and execute a large amount of stolen tasks. In contrast, at higher processor counts, such as those shown in Figure 9(b), it becomes difficult for underloaded processors to find work to be stolen, as the work per processor decreases and the pool of potential processors from which to request increases. Experimentally, few processors are able to find work once they have exhausted their local regions. Moreover, the amount of work available for stealing also decreases. Both of these behaviors are expected for strong scaling experiments.

In general, work stealing strategies for PRM perform better than the non-load-balanced run, but not as well as repartitioning. In the breakdowns shown in Figure 7, we can see that the node connection phase does not improve to the extent of repartitioning, due to the random and non-exact nature of work stealing and various overheads involved. However, region connection was not affected to the degree seen with repartitioning because the method ultimately did not move a large number of regions and thus the number of edge cuts were not affected as severely.

RRT. We evaluated work stealing on the radial RRT parallel motion planning algorithm. Figure 10 shows the total execution time for computing the final RRT for a rigid body robot in two cluttered environments and one free environment on OPTERON-CLUSTER. We varied the amount of free space in each environment such that the first environment (mixed) is 60% blocked, the second environment (mixed-30) is 30% blocked and the third environment (free) is completely free of obstacles (0% blocked). Using the DIFFUSE work stealing strategy allowed the algorithm to achieve a speedup of 2.0x on 32 cores and 1.55x at 256 cores in the mixed environment. A similar pattern of decreasing marginal benefit of work stealing from uniform subdivision is exhibited in this experiment. As with uniform subdivision, the stealable work per processor decreases with the number of processors, while the number of potential victims from which to steal also increases.
We find that all three work stealing strategies show similar improvements in execution time, as the problem is overdecomposed to such a degree that underloaded processors have a high probability of finding work, regardless of the victim selection. As expected, work stealing shows a larger improvement in the mixed environment vs. the mixed-30 environment, as the reduction in execution time is a function of the amount of imbalance. Similar to the PRM experiments in which we measure load balancing overheads, we find that in the free environment, we do not see significant differences in the load-balanced execution vs. the baseline.

V. RELATED WORK

Parallel Motion Planning. For over three decades, researchers have proposed and studied different types of parallel algorithms for motion planning problems. For a comprehensive survey of early work in parallel motion planning, please see [17]. Recently, research efforts have focused on parallel sampling-based motion planning. These renewed efforts are in part encouraged by the progress made in sequential algorithms, the ubiquity of parallel and distributed machines, and the demand for more efficiency in solving motion planning problems.

In [1], the authors make a case for the “embarrassingly” parallel nature of PRM and present a parallel algorithm in which each processor generates an “equal” number of samples and attempts connection between each sample and its k-nearest neighbors. The original shared-memory algorithm was later applied to protein folding [31] and extended for distributed-memory machines. The major drawback of this approach was the all-to-all communication involved in the global search to find nearest neighbors, limiting scalability to large systems or problem sizes.

Carpin and Pagello [7] propose parallelizing RRT computations on shared-memory machines. Bialkowski et al. [2] provide a parallel GPU-based RRT and RRT∗ by focusing on parallelizing the collision detection phase. A more recent work focused on multicore architectures [11]. The authors present three algorithms for distributed RRT. The first algorithm is a message passing implementation of the OR parallel paradigm. In the second algorithm, each process builds part of a tree and globally communicates with the other processes each time a new node and edge is added. The third algorithm adopts a manager-worker approach where the manager initializes a single tree, while the expansion computation is delegated to the workers. However, this approach does not scale well as it is prone to load imbalance.

Load Balancing and Work-Stealing. Load balancing in parallel computations is a well-studied problem. Work stealing has become the de facto dynamic scheduling technique for various parallel programming environments and runtimes, including Cilk [3], TBB [18], UPC [13] and many others. Blumofe [4] shows that work stealing is provably optimal within a constant factor for scheduling multithreaded computations with dependences. These approaches prove
successful in shared-memory architectures, but have their limitations when applied to distributed-memory. For shared-memory, the issue of locality is generally not stressed. Recently, locality-aware work stealing implementations began placing more emphasis on the notion of affinity [15] and have been shown to perform well in practice.

The X10 programming language [9] and runtime systems offer work stealing in distributed-memory architectures. Particularly, X10’s lifeline work stealing approach has shown success in balancing load for various applications, including the UTS [26] benchmark. Chapel [6] is a parallel programming language that runs in distributed-memory and provides support in work stealing scheduling.

Charm++ [21] is a parallel programming language and runtime environment that supports a large suite of load balancing mechanisms. In the Charm programming environment, computations are expressed as objects that represent both the work and associated data. In such a model, the work and data are inherently coupled, making it difficult to reason about a data structure or describe a computation in a parametric and data-independent fashion.

In addition to work stealing, other popular approaches for load balancing include partitioning tools for meshes, arbitrary graphs and other data structures. Zoltan [12] and ParMetis [22] are just a few such frameworks that provide repartitioning algorithms and data management tools.

VI. CONCLUSION

This work reviewed sampling-based motion planning methods in sequential processing and two parallel algorithms based on PRM and RRT. We highlighted load imbalance in these algorithms and presented load balancing techniques to address these issues. This work presented a theoretical model analyzing the efficacy of these load balancing techniques and showed that using repartitioning allows for achieving a distribution of load close to the ideal. In addition, we compared repartitioning and work stealing, finding that work stealing is a suitable approach for all cases, but does not perform as well as repartitioning. In contrast, we showed that repartitioning is extremely effective in balancing load for parallel sampling-based motion planning, but is not applicable to RRT-based algorithms as it is to PRM-based algorithms. Finally, we validated our approach on two massively parallel architectures more than 3,000 cores.

REFERENCES