

ECHO: Instantaneous In Situ Race Detection in the IDE

Sheng Zhan, Jeff Huang
Parasol Laboratory
Texas A&M University, USA
echo@tamu.edu, jeff@cs.tamu.edu

ABSTRACT

We present ECHO, a new technique that detects data races instantaneously in the IDE while developers code. ECHO is the first technique of its kind for incremental race detection supporting both code addition and deletion in the IDE. Unlike conventional static race detectors, ECHO warns developers of potential data races immediately as they are introduced into the program. The core underpinning ECHO is a set of new change-aware static analyses based on a novel static happens-before graph that, given a program change, efficiently compute the change-relevant information without re-analyzing the whole program. Our evaluation within a JAVA environment on both popular benchmarks and real-world applications shows promising results: for each code addition, or deletion, ECHO can instantly pinpoint all the races in a few milliseconds on average, three to four orders of magnitude faster than a conventional whole-program race detector with the same precision.

CCS Concepts

•Software and its engineering → Software maintenance tools; Software verification and validation;

Keywords

Data Races, Change-aware, Instantaneous Detection, IDE

1. INTRODUCTION

Data races are among the hardest to debug types of bugs in software systems. As software becomes more parallel, race detection techniques are proliferating [1, 2, 3, 4, 5, 6]. Several industrial-strength tools [7, 8, 9] have also been deployed. Most techniques and tools, however, are designed for late phases of the software development cycle, *e.g.*, testing or production, where the whole program is completed. Although races detected in a later phase are more likely to be real bugs, scaling to programs with a large code base without sacrificing detection coverage or accuracy is diffi-

Permission to make digital or hard copies of all or part of this work for personal or classroom use is granted without fee provided that copies are not made or distributed for profit or commercial advantage and that copies bear this notice and the full citation on the first page. Copyrights for components of this work owned by others than ACM must be honored. Abstracting with credit is permitted. To copy otherwise, or republish, to post on servers or to redistribute to lists, requires prior specific permission and/or a fee. Request permissions from permissions.acm.org.

FSE'16, November 13-19, 2016, Seattle, WA, USA

© 2016 ACM. ISBN 978-1-4503-4218-6/16/11...\$15.00

DOI: <http://dx.doi.org/10.1145/2950290.2950332>

```
15 public static void main(String[] args){
16     MyThread t = new MyThread();
17     t.start();
18     synchronized(lock){
19         y = 1;
20         x = 1;
21 Race: Example.x (Example$MyThread:37, Example:20)
22     try{
23         t.join();
24         //use z - may throw divide by zero exception
25         System.out.println(1/z);
26     }
27     catch(Exception e){
28         e.printStackTrace();
29     }
30 }
31 static class MyThread extends Thread{
32     public void run(){
33         int r1,r2;
34         synchronized(lock){
35             r1 = y;
36         }
37         r2 = x;//race here, x is not protected
38         if(r1+r2>0) {
39             z=1;//authenticate z here if x or y is p
40         }
41     }
42 }
```

Figure 1: Instantaneous race detection by ECHO.

cult. Moreover, the later a bug is found, the more expensive it would be to fix it [10].

We advocate detecting races early in the programming phase (ideally, in the IDE) such that it is both easier to scale the race detector, by amortizing the analysis cost, and cheaper to fix the detected races, by providing developers early feedback. However, existing IDEs (*e.g.*, Eclipse [11]) lack the support for detecting sophisticated bugs such as data races, because of the expensive analysis cost. For example, static race detectors [1, 2, 12] typically require pointer analysis, which often takes several seconds or minutes to compute for realistic programs. Upon a code change in the IDE, instead of running a conventional race detector and waiting for seconds or minutes, developers would favor an in situ race checker running in the background that, similar to checking syntax errors, detects races “instantaneously” as they are introduced, and as non-intrusively as possible.

In this paper we present ECHO, a new technique and a prototype tool that realize the above vision in Eclipse. A snapshot of ECHO is shown in Figure 1 (see also a video demo at [13]). The two statements at lines (20,37) form a race on a shared variable *x*. ECHO detects this race and displays the bug warning instantly (*i.e.*, in a few milliseconds) as the two statements are introduced into the program. When either of the two statements is deleted or the statement at line 37 is moved into the synchronized region, ECHO will invalidate the warning, again, instantly.

In a nutshell, ECHO leverages the fact that programming often involves frequent but small changes, which can be analyzed quickly together with only their respective dependencies without re-analyzing the whole program. Yet, for race detection, the problem of how to efficiently update the change effects and correctly relate them to races is quite challenging. We develop a new *change-aware* race detection algorithm based on a novel graph representation of the happens-before relation that handles a realistic subset of multithreaded JAVA programs (see Section 3), supporting both addition and deletion of different types of statements.

A critical component of our algorithm is an on-the-fly points-to analysis that determines the heap locations accessed by program statements and pointer aliases for reasoning about lock operations. Although points-to analysis has been intensively studied before [14, 15, 16] including a few incremental algorithms [17, 18], there is no previous technique that is applicable within an IDE in which both code addition and deletion must be handled. In particular, handling *deletion* is difficult because it may involve complex data-flow analysis and invalidation of the existing points-to set. A reset-then-recover algorithm does not scale because the analysis is cubic in the program size. We develop a novel *reachability-based* algorithm that optimizes the invalidation of the points-to set when a statement is deleted, achieving as much as 41X (see Section 4.1) speedup over the reset-then-recover algorithm on a real-world application.

Like other static race detectors, ECHO is incomplete and can report false positives due to the limitation of static analysis. However, compared to conventional race detectors, we argue that ECHO is less over-whelming to developers as they receive immediate feedback on potential races rather than getting a large number of warnings all at once. Moreover, ECHO implements two optimizations to improve precision. First, ECHO uses a hybrid algorithm combining happens-before and lockset. As observed by other researchers [19, 1], the hybrid algorithm is effective in pruning false positives reported by purely lockset-based detectors. Second, ECHO builds on top of an object field-sensitive (but context-insensitive), *locally* flow-sensitive Andersen-style analysis [20]. The field and locally flow sensitivity effectively reduces false positives caused by object-level false sharing and flow-insensitivity within a method.

Our evaluation on a variety of popular benchmarks and real-world applications shows that ECHO detects 100% of the known races with a 36% false positive ratio and it takes only 1-5ms on average to handle each change. Compared to a whole-program race detector with the same recall and precision, ECHO is three to four orders of magnitude faster.

We highlight our contributions as follows:

- To our best knowledge, ECHO is the first static race detection technique that makes instantaneous in situ race detection possible in the IDE.
- We present a set of novel change-aware static analyses including efficient data structures and new points-to analysis algorithms that enable ECHO to quickly respond to both code addition and deletion.
- We present an evaluation of ECHO on both popular multithreaded benchmarks and real-world JAVA applications and demonstrate that it can pinpoint races in milliseconds with a reasonable precision. We also identify three common sources of false positives.

2. OVERVIEW

We first present an overview of ECHO with an artificial example, and then discuss the technical challenges.

Example. Imagine that in an IDE the developer has written the JAVA program in Figure 2(a) but not yet the code in the gray region (*i.e.*, the changes ①–⑤). The program starts two threads testing a *Vector* container by storing and retrieving objects of the *Conference* class, which has two attributes, *name* and *year*. The main thread (*T1*) first creates two *Conference* objects, *c1* and *c3*, and assigns *c1* to another object reference *c2*. It then adds *c2* to the vector *v* and starts the child thread (*T2*) passing *v* as an argument. *T2* traverses the vector and prints out each element contained in it. The *Vector* implementation here is not thread-safe, because its methods are not synchronized. However, there is no data race in this program so far, because all operations are ordered by *happens-before*, *i.e.*, *T2* must execute after the thread start operation by *T1* at line 39.

2.1 ECHO in Action

Suppose the developer now performs the changes ①–⑤. We next show how ECHO reacts to them one by one.

Change ①. When the first change ① `v.add(c3)` is introduced, ECHO displays two races between lines (19,23) and (23,27). The reason is that the change adds the second *Conference* object (referenced by *c3*) to the vector, which modifies both the size of the vector (`count`) and the corresponding array element (`elems`) at line 23. These two writes are not ordered with the two method calls `v2.size()` and `v2.get(i)` by *T2*, which respectively read `count` and `elems` on the same vector at lines 19 and 27. None of these four accesses is protected by any lock and they form two races.

Changes ②③. Upon seeing the two race errors, the developer attempts to fix them by introducing Change ②: adding `synchronized` keyword to both `add(e)` and `size()`. ECHO detects that the race (19,23) is fixed (because both of the two accesses are now protected by the same lock) and clears the warning. However, the other race (23, 27) remains because the access at line 27 is not protected. As a result, the developer proceeds to introduce Change ③: adding `synchronized` to `get(i)`. After this change, the race warning (23,27) also disappears because ECHO detects that both accesses to the array element are now protected by the same lock.

Changes ④⑤. Now the developer adds ④ `c3.incrementYear()` at line 41. ECHO detects a new race (8,10), because this method call modifies the attribute `year` at line 8 and it is not ordered with the method call `p.toString()` by *T2* at line 51, which reads `year` at line 10. Both *c3* and *p* can refer to the second *Conference* object and these two methods are not synchronized, so the two accesses to `year` form a real race. To fix this race, instead of adding synchronization, the developer realizes that *c3* should not be added to the vector and hence performs Change ⑤: deleting `v.add(c3)` at line 40. Upon the deletion, ECHO invalidates the race warning (8,10) because now lines 8 and 10 access different objects and *p* cannot refer to the second *Conference* object.

2.2 ECHO in a Nutshell

Figure 2(b) shows an architectural overview of ECHO, consisting of three components: a change tracker, a race detection engine, and a race displayer. The first and the third components are both IDE-specific. The second component takes one or more changes as input and runs a change-

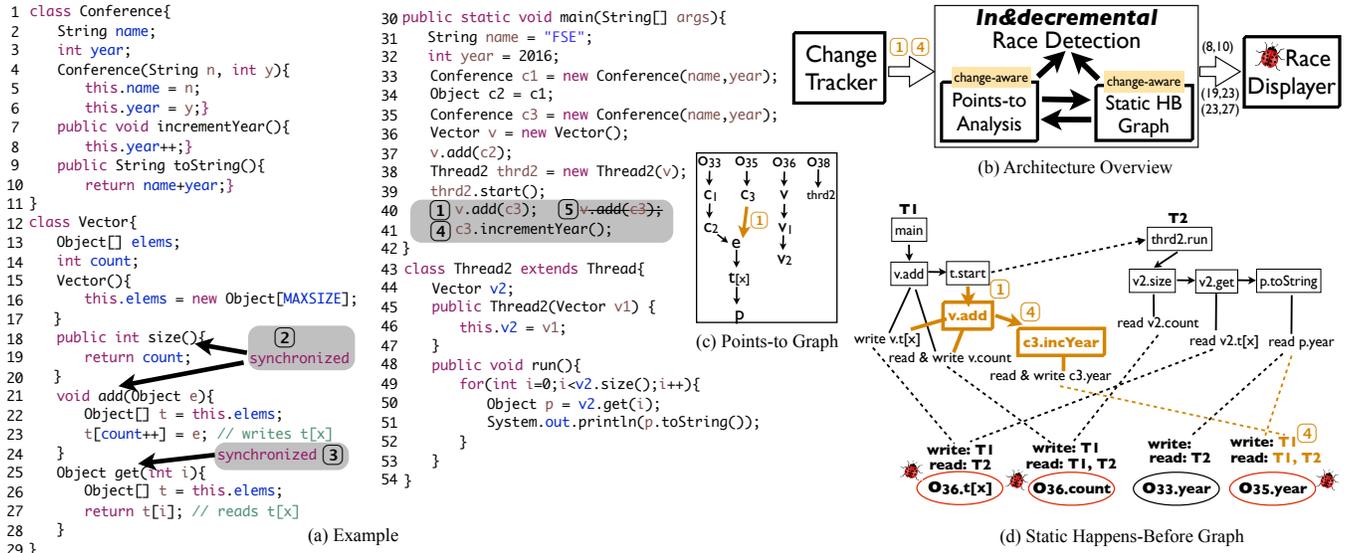


Figure 2: ECHO Technical Overview.

aware algorithm to detect races. The algorithm relies on three mutually dependent graphs – a points-to graph, a call graph, and a static happens-before (SHB) graph, all of which are computed in a *change-aware* manner: only those facts (nodes and edges) in the graph that are affected by the change are recomputed and the rest of the graph remains the same. The points-to graph and call graph are standard. We refer the readers to previous work [14, 21] for their background. The SHB graph is a new data structure:

Static Happens-Before (SHB) Graph. The SHB graph augments the call graph with directed edges representing the happens-before relation between abstract threads and heap accessing statements: read or write to *abstract heap locations* (AHL) – a field of an abstract object or an array element. Figure 2(d) shows the SHB graph of the example program. The edge $v.add \rightarrow t.start$ means that $v.add$ should be executed before $t.start$ and $t.start \rightarrow thrd2.run$ because $thrd2.run$ can only execute after the thread is started. The edges are transitive. In addition, for the race detection algorithm to identify *conflicting accesses* to AHL, the SHB graph is also associated with two states from each heap access statement to the corresponding AHL: a read set and a write set, denoting reads and writes to the AHL, respectively. For example, the method $v.add$ by $T1$ writes to $t[x]$ and $count$ on v . Because v can point to o_{36} , these two writes by $T1$ are included in the write sets of $o_{36}.t[x]$ and $o_{36}.count$, respectively.

Algorithm 1 ECHO Race Detection (Δ_P)

- 1: **Input:** Δ_P - a set of program changes.
Additions: $\{a_1, a_2, \dots\}$; deletions: $\{d_1, d_2, \dots\}$.
- 2: **Global states:** ptg - points-to graph;
- 3: cg - call graph;
- 4: shb - static happens-before graph.
- 5: $\Delta_{ptg}, \Delta_{cg} \leftarrow \text{UpdatePointsToAndCallGraph}(\Delta_P)$;
- 6: $\Delta_{shb} \leftarrow \text{UpdateSHBGraph}(\Delta_P, \Delta_{ptg}, \Delta_{cg})$;
- 7: **DetectDataRaces**($\Delta_{shb}, \Delta_{ptg}$).

Algorithm Overview. An overview of our race detection algorithm is shown in Algorithm 1. Given a set of pro-

gram changes Δ_P (including both addition and deletion¹), the points-to graph and call graph are updated first (we will show how in Section 3). Then, Δ_P together with the changes in the call graph (Δ_{cg}) are used to update the SHB graph. Finally, we check if any update in the points-to graph (Δ_{ptg}) and the SHB graph (Δ_{shb}) can lead to new races or invalidate any existing races. There are two basic steps: *finding and invalidating conflicting accesses* and *checking happens-before and lockset*. The first step tracks changes of states (*i.e.*, read and write sets) associated with each AHL. If the write set contains accesses from at least two different abstract threads, or at least one access from a thread that is different from any thread in the read set, the two corresponding accesses are considered conflicting. The second step checks for the two conflicting accesses their happens-before relation and locksets, which can be computed using the SHB graph. If the two accesses are not ordered by happens-before or their locksets do not overlap, they are reported as a race.

Example. The points-to graph and the SHB graph before the changes ①–⑤ are shown in Figure 2, ignoring the colored nodes and edges. When ① $v.add(c_3)$ is added, both of the two graphs are updated. A new edge $c_3 \rightsquigarrow e$ is added in the points-to graph because c_3 is passed as the method argument. A new node $v.add$ is added in the SHB graph and a new edge from $t.start$ is added to this new node. In addition, because p can now refer to o_{35} , the read set of $o_{35}.year$ is updated to include $T2$. The new $v.add$ node has the same read and write statements as that of the first $v.add$ node before $t.start$. However, the difference is that this node is not ordered with the nodes from $T2$. Hence, by checking happens-before and lockset related to the new accesses to $o_{36}.t[x]$ and $o_{36}.count$, our algorithm detects two races, (19,23) and (23,27). Similarly, when ④ $c_3.incrementYear()$ is added, a new $c_3.incYear$ node is added to the SHB graph and a new edge from $t.add$ is added to the new node. Both the read and write sets of $o_{35}.year$ are updated to include $T1$, because $c_3.incrementYear()$ reads and writes the field

¹Note that a code update can be treated as two changes: deletion of the old statement, and addition of the new statement. Large code chunks can be treated as a sequence of small changes.

P	::=	$defn^* e$	(program)
$defn$::=	$class C \{field^* meth^*\}$	(class decl)
$field$::=	$T f$	(field decl)
$meth$::=	$C m(arg^*)\{s^* return z\}$	(method decl)
s	::=		
		❶ $x = new C$	(allocate)
		❷ $x = y$	(simple assign)
		❸ $x = y.f$	(field read)
		❹ $x.f = y$	(field write)
		❺ $x = o.m(arg^*)$	(method call)
		❻ $x = y[i]$	(array read)
		❼ $x[i] = y$	(array write)
		❽ $t.start()$	(thread fork)
		❾ $t.join()$	(thread join)
		❿ $synchronized(x)\{s^*\}$	(lock)
		Ⓢ $loop(b)\{s^*\}$	(loop)
		$if(b)\{s^*\}$	(conditional)
		e	(other)

Figure 3: SIMJava.

year, and c3 can refer to o_{35} . The read set of $o_{35}.year$ contains $T2$, so the two accesses at lines 8 and 10 are conflicting. By checking their happens-before and locksets, our algorithm detects a new race (8,10).

2.3 Technical Challenges

To achieve both fast speed and good precision, there are several tough technical problems that we must solve:

1. **Change-aware race detection.** How to correctly and efficiently react to a program change? How to correctly handle different types of changes? How to efficiently maintain the happens-before relation and lockset upon a change?
2. **Change-aware points-to analysis.** How to soundly update the points-to graph and call graph upon a change? By soundness, we mean that any true points-to relation must be represented in the points-to graph. Meanwhile, we would like to compute a points-to graph that is as precise as possible. For example, in Figure 2, before Change ④, the variable p cannot refer to o_{35} . Otherwise, a false positive would be reported.
3. **Sound static happens-before graph.** How to construct a sound SHB graph such that any true happens-before relation is represented and no reachable heap access is missed? How to handle back edges caused by loops or recursion? How to identify abstract threads that may have multiple runtime instances?

We next present our algorithms to address these challenges.

3. ALGORITHM

We first introduce a multithreaded language SIMJava, which contains a subset of JAVA basic constructs for multithreaded programming. Based on SIMJava, we then present our change-aware analysis algorithms.

The SIMJava Language. SIMJava is inspired from CONCURRENTJAVA [22], with a few differences and extensions that make it more powerful for expressing concurrency and also cleaner for change-aware analysis. Figure 3 shows the syntax. SIMJava supports three types of inter-thread synchronization operations, fork, join, and synchro-

Table 1: Static HB Graph Construction. Array accesses are treated similarly to field accesses (as to a single field) and are omitted.

Statement	Nodes
❸ $x = y.f$	$read(y.f)$
❹ $x.f = y$	$write(x.f)$
❺ $x = o.m(y^*)$	$\forall O_c \in pts(o): call(O_c.m)$
❽ $t.start()$	$\forall O_c \in pts(t): fork(O_c)$
❾ $t.join()$	$\forall O_c \in pts(t): join(O_c)$
❿ $synchronized(x)\{s^*\}$	$\forall O_c \in pts(x): lock(O_c)$ $unlock(O_c)$
Ⓢ $loop(b)\{s^*\}$	unroll twice: $s^* s^*$
Two fake nodes: $\forall O_c \in pts(t): start(O_c) \& end(O_c)$	
Edges: $\forall O_c \in pts(t): fork(O_c) \rightarrow end(O_c)$ $end(O_c) \rightarrow join(O_c)$	
Method call $s(o.m)$:	
$\forall O_c \in pts(o): \Rightarrow Node(s) \rightarrow FirstNode(O_c.m)$ $LastNode(O_c.m) \rightarrow NextNode(s)$	
$\forall s_1, s_2 \in m \& s_1 \prec s_2 \Rightarrow Node(s_1) \rightarrow Node(s_2)$	

nized. The fork and join operations together form the inter-thread happens-before relation and `synchronized` forms the lock mutual exclusion relation. Statements ❶-❿ include the typical operations for object allocation, assignments, field and array read and write, method invocation, as well as the concurrency primitives. All these statements are analyzed in our race detection. In addition, SIMJava supports the `loop` operation Ⓢ `loop(b){s*}` that evaluates a boolean variable `b` and iterates the statements `s*`. The `loop` operation reflects loop constructs, such as `for` and `while`. For race detection, `loop` operations must be considered in our algorithm because they may spawn multiple threads and may also introduce multiple other types of synchronizations. However, conditionals are ignored because our algorithm is path-insensitive.

3.1 Change-aware Data Race Detection

The core of our algorithm is a change-aware static happens-before (SHB) graph powered by an on-the-fly points-to analysis. We first present the SHB graph construction algorithm.

3.1.1 SHB Graph Construction

Starting from a unique entry method (e.g., `main`), the SHB graph is constructed following the rules in Table 1. There are nine different types of nodes in the SHB graph, corresponding to the nine statements ❸-❿ in SIMJava. ❸ (field read) and ❹ (field write) are similar to ❺ (array read) and ❼ (array write), except that the index to field is fixed and bounded, but to array it is not. To improve efficiency, array index is often ignored in static analysis. We also do not distinguish different array elements, instead we create a single “field” `x` for each array object `a` and consider all accesses to `a` as to `a[x]`. In this way, ❸ and ❼ are equivalent to ❺ and ❼, respectively. We hence omit the discussion of ❸ and ❼ in the rest of this paper.

The synchronization statement ❿ (`synchronized(x){s*}`) generates two nodes: `lock` and `unlock`, inserted at the beginning and end of the synchronized block, marked by “{” and “}”, respectively. In addition, we introduce two fake nodes for each abstract thread: `start` and `end`, which are used to construct the inter-thread happens-before relation.

Abstract Heap Location (AHL). For field and array

read/write node, we maintain a *link* from the node to one (or more) AHL, which corresponds to the abstract data the node accesses. The AHL is identified by $O.f$ (for field) or $O.x$ (for array), where O is an abstract object in the points-to set of the base variable. Each AHL is associated with two states: a read set and a write set, recording write and read accesses to the location from abstract threads. This information is used to identify conflicting heap accesses.

Abstract Thread and Lock. For the other nodes, each node is associated with one (or more) abstract object, which is computed using the points-to set of the corresponding base variable. For example, for `fork` and `join` (and `start` and `end`), their abstract object is the corresponding abstract thread, identified by the points-to set of `t` in `t.start()`, `t.join()`, or the `main` method (for only the `main` thread). For `lock` and `unlock`, their abstract object is the corresponding abstract lock: the points-to set of `x` in `synchronized(x)`.

Handling Loop. For loop statements \textcircled{L} (`loop(b){s*}`), we create more than one sequence of nodes for s^* , because each s may generate multiple reads/writes or fork multiple threads. The challenge is that the number of loop iterations is unknown statically. Nevertheless, for race detection, it suffices to unroll the loop twice. The reason is that data races involve only two abstract threads and two memory accesses. Unrolling a loop twice will guarantee to expose the same set of races as unrolling more than two times. Similarly, we handle recursion by unrolling all loops in the call graph twice and removing the corresponding back edges.

Statement Location. Each node in the SHB graph is also associated with a unique location, corresponding to the program location of the statement. The unique location is used to determine the program order for statements from the same method. For synchronized blocks and loop statements, the locations of their corresponding nodes are treated in the following way. For `lock` and `unlock`, their locations correspond to the locations of “{” and “}” of the synchronized block. For loops, we add a loop iteration identifier (either I1 or I2) to each node, all nodes with I1 should happen before nodes with I2 unrolled from the same loop statement. Together with the call graph, the node location information is used to compute the intra-thread happens-before relation.

Happens-Before. The happens-before edges are constructed over `fork`→`start` and `end`→`join` for each abstract thread object and over method calls. For a method call s , $o.m$, for each abstract object, O_c , in the points-to set of o , an edge is added from its corresponding node, `Node(s)`, to the first node of the callee method, `FirstNode($O_c.m$)`. In addition, an edge is added from the last of the callee method, `LastNode($O_c.m$)`, to the next node of s , `NextNode(s)`. Furthermore, happens-before edges are added between consecutive nodes in $O_c.m$ following the program order.

Lockset. `lock` and `unlock` nodes do not introduce happens-before edges. Instead, we associate every memory access node (`read` and `write`) protected by each pair of `lock` and `unlock` nodes with a **lockset** and add all the abstract lock objects to which these `lock` variables may refer to the lockset. The lockset is used together with happens-before to improve precision of race detection.

3.1.2 Change-aware SHB Algorithm

If any of these nine types of statements is added or deleted, our change-aware SHB algorithm updates the SHB graph. This step is relatively straightforward by following the rules

in Table 1. For addition, we first insert the corresponding nodes (introduced by the new statement) into the SHB graph according to the statement location. We then add the links for AHL and add the happens-before edges according to the points-to set. For deletion, we simply remove all the corresponding nodes and their links and edges from the graph. If a removed node n is between two nodes $n_1 \rightarrow n \rightarrow n_2$, then the two nodes will be connected $n_1 \rightarrow n_2$. For loop statements, their addition and deletion are equivalent to adding and deleting s^* in the loop body. For synchronization statements, we update the lockset of each `read` and `write` node that they protect accordingly.

For statements $\textcircled{1}$ (allocate) and $\textcircled{2}$ (simple assignment), they may change the points-to graph and call graph. For points-to changes, we update the SHB graph by adding or deleting the corresponding nodes/edges/links according to the changed points-to set of each base variable. For call graph changes, we delete only the related edges but not the nodes, to reuse the nodes later if a method call statement to the same method is added.

A caveat is that a statement may appear as multiple nodes in the SHB graph because the statement is in a loop or its enclosing method is called in multiple places. Therefore, for changes to these statements, we must track and update all their occurrences in the SHB graph. We track these statements by maintaining a map from each method to its locations in the graph and a boolean state for each statement indicating if it is in a loop. For a statement change, we locate all their occurrences by checking both the map with its enclosing method, and the boolean state. We do not handle method recursion separately because recursion is already handled by unrolling the loops twice in the call graph.

3.1.3 Change-aware Race Checking

The race checking procedure is triggered upon a change in the SHB graph. There are three types of changes: links to AHL, lockset, and happens-before. When a link to an AHL is added or deleted, it means that a `read` or `write` node, X , is added or deleted, and we perform race checking specific to X . We first find all the pairs of conflicting nodes including X by checking the associated read and write sets. Because any of these pairs may become a race (or no longer a race), for each pair, we check the happens-before relation and the lockset condition between the two nodes. If the two nodes cannot reach each other on the SHB graph and their locksets do not intersect, we flag them as a race. The lockset condition here is essentially a may-alias analysis that determines if two lock variables may refer to a common lock. If the flag of any pair is changed, we update the race warning in the IDE.

For happens-before changes, we only handle inter-thread changes because intra-thread happens-before is determined by program order and it alone cannot introduce new races or invalidate existing races. There are two types of inter-thread happens-before edges: (1) `fork`→`start` and (2) `end`→`join`. For (1), we check only the conflicting node pairs involving those nodes that happen before the `fork` node and those that happen after the `start` node because only (the happens-before relation of) those nodes can be affected by this happens-before edge. Similarly, for (2), we check only the conflicting node pairs involving those nodes that happen before the `end` node and after the `join` node. For lockset changes, similarly, we only find and check those conflicting node pairs involving nodes whose locksets are changed.

Table 2: Extended Andersen’s Algorithm (O_c refers to abstract objects of type C).

Statement	Points-to Set Constraint	Points-to Graph Edge
① $x = \text{new } C$	$O_c \in pts(x)$	$O_c \rightsquigarrow x$
② $x = y$	$pts(y) \subseteq pts(x)$	$y \rightsquigarrow x$
③ $x = y.f$	$\forall O \in pts(y) : pts(O.f) \subseteq pts(x)$	$O.f \rightsquigarrow x$
④ $x.f = y$	$\forall O \in pts(x) : pts(y) \subseteq pts(O.f)$	$y \rightsquigarrow O.f$
⑤ $x = o.m'(y^*)$ //from method $C.m$ call: $m'(y^*)\{s^* \text{ return } z\}$	$pts(y) \subseteq pts(y')$ and $pts(z) \subseteq pts(x)$ $\forall O_{c'} \in pts(o) : \text{add } (C.m, cs) \rightsquigarrow C'.m'$ in the call graph //cs – call site	$z \rightsquigarrow x$ and $y \rightsquigarrow y'$

3.2 Change-aware Points-to Analysis

Our change-aware points-to analysis builds on an on-the-fly Andersen-style algorithm [20]. It is context-insensitive, but field-sensitive and locally flow-sensitive, *i.e.*, flow-sensitive within each method. The key novelty of our new algorithm (Algorithm 2) is to make the analysis more efficient in handling program changes including both addition and deletion.

On-the-fly Andersen’s Algorithm. Let $pts(v)$ denote the points-to set of a variable v and O_v the abstract object directly assigned to v . Points-to analysis is often cast as a graph closure problem. Each node represents a variable v and has an associated points-to set $pts(v)$ or O_v . In Andersen’s algorithm, edges represent *subset constraints* between nodes: an edge $a \rightsquigarrow b$ means that $pts(a)$ is a subset of $pts(b)$.

For SIMJAVA, there are seven types of statements (①-⑦) relevant to points-to analysis. ① (allocate) and ② (simple assignment) are used to initialize the points-to graph, and the rest five (③-⑦) may add more edges on-the-fly. Table 2 shows an extended Andersen’s algorithm. The statement ⑤ (method call) is also directly related to call graph construction. When a new call graph edge is discovered, the points-to graph may also be updated because of the new points-to facts introduced by parameter passing and value returning. As a result, the on-the-fly algorithm works in a loop until reaching a fixed point, *i.e.*, both the points-to graph and call graph are unchanged. In each iteration, a worklist is used to track the new points-to facts and the points-to information is propagated along the two graphs following the constraint rules in the second column in Table 2.

3.2.1 Statement Addition

Handling statement addition follows the same rationale as the on-the-fly Andersen’s algorithm. New points-to facts (nodes/edges) are first extracted from the added statement and put into the worklist. Then, the points-to information is computed along the relevant paths in the two graphs until reaching a fixed point. The key advantage of our new algorithm is that only those nodes in the paths related to the new facts are recomputed, all the other nodes are untouched.

The algorithm (Algorithm 2 lines 7-19) takes an added statement s and its enclosing method $C.m$ as input ($C.m$ is needed for building the call graph). It first finds out all the new edges that s may introduce using the function **FIND-EDGES** (Algorithm 3). **FIND-EDGES** handles each type of statements following the rules in the third column of Table 2. For the first two types (① and ②), the new edges can be added straightforwardly (note that the side effect of Statement ② ($x = y$) is handled by the fixed-point computation in Algorithm 2 at lines 9-19). For the other three (③④⑤), which we call *complex statements*, their corresponding edges are not fixed but depend on the points-to set of their base variable. For example, for ③ ($x = y.f$), suppose $pts(y)$ contains two objects o_1 and o_2 , then two edges must

be added: $o_1.f \rightsquigarrow x$ and $o_2.f \rightsquigarrow x$. Moreover, when $pts(y)$ is changed during the computation, the corresponding statement must be re-evaluated because new edges may be added or deleted. Therefore, in addition to finding edges for these complex statements, **FIND-EDGES** also maintains a map, CS , that records the corresponding complex statements and their methods for each base variable. For example, for ④ ($x.f = y$), $x \rightsquigarrow (C.m, s)$ is added to CS for statement addition and deleted for deletion.

Statement ⑤ ($x = o.m'(y^*)$) may additionally update the call graph, trigger new statements (in the callee method) to be added, or introduce points-to edges related to both formal parameters and return. We handle all these cases correspondingly in Algorithm 3.

For each new edge $src \rightsquigarrow dst$, if it is not already in the points-to graph, it is added to the graph and its points-to set is updated following the subset constraints: $pts(dst) \leftarrow pts(dst) \cup pts(src)$. If $pts(dst)$ is changed, all edges from dst in the points-to graph are added to the worklist and re-processed to update the points-to set of v . In addition, all the complex statements that have dst as their base variable will be re-processed because new points-to edges may be introduced by the change in $pts(dst)$.

3.2.2 Statement Deletion

Handling statement deletion is more complicated than addition. Intuitively, it is the reverse of addition and, if we can track the state changes of the points-to graph by each addition, we may undo the changes for deletion of the same statement. Yet, this intuition is not true because points-to analysis is not “reversible”. Deletion is fundamentally different from insertion in that it requires updating not only the pointer information of the specific change, but also previous changes that are dependent on this change. Moreover, for large graphs, it is expensive to memorize the state changes.

Reset-then-recover. One (less efficient) solution (Algorithm 2 lines 20-40) is to reset the points-to sets of all relevant nodes and then recompute them. We can first find all the points-to edges that are related to the deleted statement, remove all these edges, and reset (set to empty) the points-to sets of their destination nodes as well as all nodes that they can reach. Then, following the same method for addition, we can add all the edges in the remaining points-to graph that can reach the reset nodes into the worklist and repeat the fixed point computation. This method is inefficient because the points-to sets of some reset nodes may in fact remain unchanged before and after the deletion, such that all the reset-then-recover computations are wasted.

Reachability-based Algorithm. Our optimized solution is based on two observations. First, after deleting an edge $x \rightsquigarrow y$, the points-to set of y may remain the same if x is still reachable to y . Second, for any abstract object O , if O is reachable to y then O must be included in the points-

Algorithm 2 Change-Aware Points-to Analysis

```

1: Input:  $s$  – a new added or deleted statement in  $C.m$ .
2: Global States:  $ptg = \langle V_p, E_p \rangle$  – points-to graph;
3:    $cg = \langle V_c, E_c \rangle$  – call graph;
4:    $pts$  – points-to set function;
5:    $W$  – worklist;
6:    $CS$  – variable to complex statements;
7: //Addition
8:  $W \leftarrow \text{FINDEDGES}(C.m, s)$ ;
9: while  $W \neq \emptyset$  do
10:   $e \leftarrow \text{SELECT FROM } W // e: \text{src} \rightsquigarrow \text{dst}$ 
11:  if  $e \notin E_p$  then //e is a new points-to edge
12:     $E_p \leftarrow E_p \cup e$ ;
13:     $pts(\text{dst}) \leftarrow pts(\text{dst}) \cup pts(\text{src})$ ;
14:    if  $pts(\text{dst})$  changed then
15:      foreach  $(\text{dst} \rightsquigarrow v) \in E_p$  do
16:         $W \leftarrow W \cup (\text{dst} \rightsquigarrow v)$ ;
17:      //process complex statements
18:      foreach  $(C.m, s) \in CS(\text{dst})$ 
19:         $W \leftarrow W \cup \text{FINDEDGES}(C.m, s)$ ;
20: //Deletion - Reset-then-recover
21:  $\text{Reset} \leftarrow \emptyset$ ;
22:  $W \leftarrow \text{FINDEDGES}(C.m, s)$ ;
23: while  $W \neq \emptyset$  do
24:   $e \leftarrow \text{SELECT FROM } W // e: \text{src} \rightsquigarrow \text{dst}$ 
25:   $E_p \leftarrow E_p \setminus e$ ; //remove e from Ep
26:   $\text{Reset} \leftarrow \text{Reset} \cup \text{dst}$ ;
27: while  $\text{Reset} \neq \emptyset$  do
28:   $v \leftarrow \text{SELECT FROM } \text{Reset}$ 
29:   $pts(v) \leftarrow \emptyset$ ; //reset the points-to set of v
30:  foreach  $(v \rightsquigarrow \text{dst}) \in E_p$  do
31:     $\text{Reset} \leftarrow \text{Reset} \cup \text{dst}$ ;
32:  foreach  $(\text{src} \rightsquigarrow v) \in E_p$  do
33:     $W \leftarrow W \cup (\text{src} \rightsquigarrow v)$ ;
34: while  $W \neq \emptyset$  do
35:   $e \leftarrow \text{SELECT FROM } W // e: \text{src} \rightsquigarrow \text{dst}$ 
36:   $pts(\text{dst}) \leftarrow pts(\text{dst}) \cup pts(\text{src})$ ;
37:  if  $pts(\text{dst})$  changed then
38:    //process complex statements
39:    foreach  $(C.m, s) \in CS(\text{dst})$ 
40:       $W \leftarrow W \cup \text{FINDEDGES}(C.m, s)$ ;
41: //Deletion - Reachability-based
42:  $W \leftarrow \text{FINDEDGES}(C.m, s)$ ;
43: while  $W \neq \emptyset$  do
44:   $e \leftarrow \text{SELECT FROM } W // e: \text{src} \rightsquigarrow \text{dst}$ 
45:  if  $\text{IsReachble}(\text{src}, \text{dst})$  then
46:    continue;
47:  foreach  $o \in pts(\text{src})$  do
48:    //L: a set of nodes whose points-to sets may change
49:     $L \leftarrow L \cup \text{dst}$ ;
50:    while  $L \neq \emptyset$  do
51:       $v \leftarrow \text{SELECT FROM } L$ 
52:      if  $\text{IsReachble}(o, v)$  then
53:        continue;
54:       $pts(v) \leftarrow pts(v) \setminus o$ ; //remove o from pts(dst)
55:      foreach  $(v \rightsquigarrow \text{dst}) \in E_p$  do
56:         $L \leftarrow L \cup \text{dst}$ ; //propagate the change
57:      //process complex statements
58:      foreach  $(C.m, s) \in CS(\text{dst})$ 
59:         $W \leftarrow W \cup \text{FINDEDGES}(C.m, s)$ ;

```

Algorithm 3 FINDEDGES($C.m, s$)

```

1: Output:  $E$  – a set of edges, initially empty.
2: Notation:  $\otimes$  – union for addition;
3:   removal for deletion.
4: switch  $s$  do
5:  case ①  $x = \text{new } C: E \leftarrow E \cup O_c \rightsquigarrow x$ ;
6:  case ②  $x = y: E \leftarrow E \cup y \rightsquigarrow x$ ;
7:  case ③  $x = y.f$ :
8:    foreach  $O \in pts(y)$  do
9:       $E \leftarrow E \cup O.f \rightsquigarrow x$ ;
10:    $CS \leftarrow CS \otimes (y, (C.m, s))$ ;
11:  case ④  $x.f = y$ :
12:    foreach  $O \in pts(x)$  do
13:       $E \leftarrow E \cup y \rightsquigarrow O.f$ ;
14:    $CS \leftarrow CS \otimes (x, (C.m, s))$ ;
15:  case ⑤  $x = o.m'(y^*)$ :
16:    foreach  $O_{c'} \in pts(o)$  do
17:      if  $C.m \rightsquigarrow C'.m' \notin E_c$  then
18:        foreach  $s' \in C'.m'(y^*)\{s'^* \text{ return } z\}$  do
19:           $E \leftarrow E \cup \text{FINDEDGES}(C.m, s')$ ;
20:           $E \leftarrow E \cup \{y \rightsquigarrow y', z \rightsquigarrow x\}$ ;
21:           $E_c \leftarrow E_c \otimes (C.m \rightsquigarrow C'.m')$ 
22:    $CS \leftarrow CS \otimes (o, (C.m, s))$ ;
23: return  $E$ .

```

to set of y . Our new algorithm (shown in Algorithm 2 lines 41-59) hence *lazily* updates the points-to set by checking the path reachability beforehand, using the function $\text{IsReachable}(x, y)$. The algorithm updates the points-to set of y (denoted by $pts(y)$) only when $\text{IsReachable}(x, y)$ returns false and it removes an abstract object O from $pts(y)$ only when $\text{IsReachable}(O, y)$ returns false.

Consider the example in Figure 4 where the edge $x \rightsquigarrow y$ is deleted. We first check if x is still reachable to y , if yes (e.g., in the existence of the path $x \rightsquigarrow p \rightsquigarrow y$), we simply stop. If not, we go on to check the reachability from each abstract object o in $pts(x)$ to y . If o is not reachable to y , we remove o from $pts(y)$ and continue to check the reachability from o to the nodes that are reachable from y (e.g., z in the example) and propagate the removal if not reachable. Otherwise, o remains in $pts(y)$ and we stop the propagation for o . If the path $x \rightsquigarrow p \rightsquigarrow y$ does not exist, after deleting $x \rightsquigarrow y$, o_1 can no longer reach y and o_1 is hence removed from $pts(y)$. The removal is propagated to $pts(z)$ because z is reachable from y and o_1 is no longer reachable to z . However, o_2 is still reachable to y via the path $o_2 \rightsquigarrow w \rightsquigarrow q \rightsquigarrow y$ and o_2 remains in $pts(y)$. The computational improvement here is that we can skip the propagation to $pts(z)$, because $pts(y)$ is unchanged.

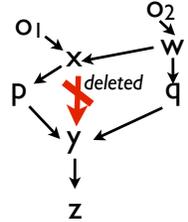


Figure 4: An example of edge deletion.

3.3 Soundness and Other Optimizations

Soundness. Both of our change-aware happens-before graph construction and points-to analysis algorithms are sound. Our incremental computation on the happens-before graph and points-to graph produces the same state as the one-shot whole-program computation. The key to the proof is that adding/removing a statement computed by our algo-

Table 4: Race Detection Performance. ECHO is typically four orders of magnitude faster than conventional whole program race detector.

	Pointer analy. (one-time)	Whole-prog. detector	ECHO	
			Average	Worst
Example	3.3s	3.3s	0.27ms	153ms
FileWriter	4s	4.1s	0.96ms	837ms
Loader	2.4s	2.5s	0.27ms	197ms
Manager	2.5s	2.5s	0.32ms	269ms
MergeSort	2.9s	4.3s	0.3ms	238ms
Racey	2.5s	2.6s	0.25ms	226ms
Pool	2.4s	2.4s	0.24ms	122ms
WebLech	16.4s	16.5s	5ms	6.4s
H2	5.2s	5.2s	0.89ms	1.0s

gorithms leave the states of the two graphs the same.

However, our race detection algorithm is unsound, due to its use of may-alias for reasoning about lockset. May-alias may incorrectly determine that two different lock variables (that refer to different locks at runtime) alias each other and certain real races may be missed. Nevertheless, this issue rarely happens in practice when the points-to analysis is locally flow-sensitive. In our studied benchmarks, ECHO can detect all known real races.

SCC Optimization. Precise tracing of all effected edges and nodes can be expensive due to the presence of cycles in the points-to graph. However, all nodes in the same cycle are guaranteed to have identical points-to sets, so we can collapse those strongly-connected components (SCC) into a single node. Previous research [21] has shown that the SCC optimization is effective scaling to millions lines of code. We also apply this optimization in our algorithm. Specifically, we maintain a super node for each SCC and update the super nodes after the initial worklist is set. A super node can be either augmented (for addition) or shrunk or broken (for deletion). Once updated, both the graph traversal and the update of points-to set in the SCCs become faster.

4. EVALUATION

We implemented ECHO as an Eclipse plugin based on the WALA framework [23]. We modified the ZeroOneCFA [20] implementation of Andersen’s algorithm (that identifies abstract objects by allocation sites). We evaluated ECHO on a variety of popular JAVA multithreaded benchmarks collected from previous concurrency studies [1, 4, 24], including three real-world systems – the H2 database [25], the WebLech Spider [26], and the Apache Commons Pool library. Our evaluation answers two sets of research questions:

1. **Performance** – How efficient is ECHO in reacting to program changes? How scalable is ECHO in detecting races? How much speed up compared to whole-program race detection?
2. **Recall & Precision** – Can ECHO detect all real races? How precise is it? What is the false positive ratio and what are the reasons for false positives?

Benchmarks. Table 3 summarizes the static characteristics of the benchmarks including our example program in Figure 2. The size of the smaller benchmarks ranges from 68 to 2.8K lines of code. The size of real-world programs ranges from 10K to 172K. All benchmarks except *H2* are self-contained, *i.e.*, each of them has a single entry point (the `main` method) for the analysis. For *H2*, we use the

test driver class `org.h2.test.synth.thread.TestMulti` in the H2 test suite as the entry point. All JDK libraries (e.g. `java.lang.*` and `java.util.*`) are included in the analysis except those excluded by WALA by default, such as `java.awt.*` and `sun.*`. These JDK libraries account for the majority of the classes (the number reported in Column 3) for building the class hierarchy. Nevertheless, for points-to analysis, only those methods that are reachable from the entry points are analyzed. The number of reachable methods for these benchmarks ranges from 927 to 3.7K, containing 27K to 108K SSA instructions. The points-to graph for each benchmark contains 9K-41K pointer keys (*i.e.*, reference variables), 1.2K-5K instance keys (*i.e.*, object allocation sites), and 60K-2.5M points-to edges. For *WebLech*, its points-to graph is much larger and more dense than the other benchmarks. It is particularly interesting for our evaluation of scalability because Anderson’s algorithm in the worst case is cubic in the size of the graph.

Evaluation Methodology. ECHO can be used by developers starting either from an empty project or from an existing code base. We choose the latter scenario for our evaluation. For each benchmark, we run the experiment in three phases: (1) run points-to analysis and static happens-before graph construction for the whole program; (2) delete a statement and run race detection; (3) add the deleted statement back and run race detection. Phase 1 is needed only once for ECHO. Phases 2 and 3 are performed for each statement in each method.

To understand the performance improvement of our novel change-aware algorithms, we also implement a whole program race detector and compare its performance with ECHO. The whole program race detector uses the same hybrid algorithm and the same pointer analysis as we use in ECHO. The only difference is that for every change it has to re-run the whole-program analysis. Hence, the whole program race detector and ECHO have the same race detection ability (*i.e.*, report the same races with same precision and recall), except that ECHO is faster.

In addition, to show the improvement of our reachability-based algorithm for handling deletion, we compare its performance with the reset-and-recover algorithm.

All experiments were performed on an Apple Mac Pro with 2.5GHz dual-core Intel i5 processor and 4GB of memory running JAVA HotSpot 64-bit Server VM version 1.8.0.

4.1 Performance

Table 4 reports the performance of ECHO compared to the whole program race detector. Column 2 reports the time for performing the points-to analysis for each benchmark. The points-to analysis time typically ranges between 2s-5s, except for *WebLech*, which takes 16.4s because of its large points-to graph. Column 3 reports the time taken by the whole program race detector to detect races upon a change. The whole program race detector needs 2.5s-16.5s to detect races. Columns 4-5 report the average and worst-case time (including all incremental analyses and the race detection) taken by ECHO for each change.

For most benchmarks (including *H2* and *Pool*), ECHO takes less than 1ms on average per change and 1s in the worst case. Compared to the whole program race detector, ECHO is typically *four orders of magnitude* faster. The only exception is *WebLech*, which has a much larger and more dense points-to graph. However, even for *WebLech*,

Table 3: Benchmarks. JDK libraries are also analyzed except those excluded by WALA by default.

Benchmark	LOC	#Classes	#Methods	#SSAInstructions	#PointerKeys	#InstanceKeys	#PointerEdges
Example	68	7091	927	26881	8832	1178	58540
FileWriter	256	7094	1569	52568	17044	2825	324631
Loader	109	7089	938	27263	8952	1199	60133
Manager	171	7090	956	27811	9121	1221	67671
MergeSort	298	7089	971	28522	9401	1275	66694
Racey	294	7091	930	27447	8859	1179	58932
Pool	10K	7091	931	26991	8852	1182	55956
WebLech	35K	7144	3650	108176	41612	5004	2578820
H2	172K	7628	1814	66463	20506	3174	391769

Table 5: Performance of Addition and Detection. Fast-Insts: instructions that take <0.1s to handle.

	Average		Worst case		Fast-Insts%	
	delete	add	delete	add	delete	add
Example	0.4ms	0.06ms	153ms	21ms	92%	92%
FileWriter	1.7ms	0.23ms	837ms	28ms	93%	93%
Loader	0.44ms	0.07ms	197ms	29ms	92%	92%
Manager	0.5ms	0.07ms	269ms	25ms	92%	92%
MergeSort	0.53ms	0.06ms	238ms	36ms	92%	92%
Racey	0.44ms	0.06ms	226ms	34ms	92%	92%
Pool	0.43ms	0.06ms	122ms	22ms	92%	92%
WebLech	8.9ms	1.1ms	6.4s	219ms	91%	93%
H2	1.6ms	0.17ms	1.0s	79ms	93%	94%

Table 6: Performance of detection algorithms.

	Reach-based		Reset-then-recover	
	average	worst	average	worst
Example	0.4ms	153ms	0.5ms(1.3X)	507ms(3.3X)
FileWriter	1.7ms	837ms	2.4ms(1.4X)	1.3s(1.6X)
Loader	0.44ms	197ms	0.48ms(1.1X)	319ms(1.6X)
Manager	0.5ms	269ms	0.58ms(1.2X)	317ms(1.2X)
MergeSort	0.53ms	238ms	0.55ms(1.0X)	338ms(1.4X)
Racey	0.44ms	226ms	0.46ms(1.0X)	343ms(1.5X)
Pool	0.43ms	122ms	0.49ms(1.1X)	517ms(4.6X)
WebLech	8.9ms	6.4s	368ms(41X)	54.2s(8.5X)
H2	1.6ms	1.0s	3.7ms(2.3X)	1.9s(1.9X)

ECHO takes only 5ms on average for each change and 6.4s in the worst case, a 3000X speedup over the whole program race detector on average and 2.5X in the worst case.

Addition & Deletion. Table 5 compares the performance between addition and deletion. Columns 2-3 and Columns 4-5 report the average and worst case time, respectively, taken by ECHO for adding and deleting a statement. Columns 6-7 report the percentage of instructions that take ECHO less than 0.1s to handle. Overall, addition (0.06–1.1ms on average and 21–219ms in the worst case) is much faster than deletion (0.4–9ms on average and 112ms–6.4s the worst). The reason is that addition does not involve the complex invalidation of existing points-to sets. Nevertheless, *over 91% of all the statements take less than 0.1s.*

Reachability-based Deletion vs Reset-then-recover. Table 6 compares the performance between the reachability-based and the reset-then-recover algorithms for handling statement deletions. In the average case, the reachability-based algorithm takes 0.4–8.9ms per change, whereas the reset-then-recover algorithm takes 0.46–368ms. In the worst case, the reachability-based algorithm takes 0.15–6.4s, while the reset-then-recover algorithm takes 0.3–54s. On average, the reachability-based algorithm is 4.6X faster than reset-then-recover for the average case and 2.3X faster for the worst case. The speedup is more significant for the real-world systems. In particular, for *WebLech*, the reset-

Table 7: Results of detected races.

	#total	#true races	#false positives
Example	1	1	0
FileWriter	7	5	2
Loader	4	2	2
Manager	9	6	3
MergeSort	0	0	0
Racey	0	0	0
Pool	0	0	0
WebLech	15	9	6
H2	0	0	0

T1, T2

```
class Spider run():
```

```
163: URLGetter urlGetter = new URLGetter(config);
204: List newURLs = downloadURL(urlGetter...);
```

```
downloadURL():
```

```
260: urlGetter.getURL(url);
```

```
class URLGetter getURL():
```

```
63: if(failureCount>10) {
67:   failureCount=0;
}
```

```
132: failureCount++;
```

Figure 5: False positives on “failureCount” in WebLech due to the lack of object sensitivity.

then-recover algorithm takes 368ms on average and 54.2s the worst, while the reachability-based algorithm takes only 8.9ms and 6.4s, respectively, which is 41X and 8.5X faster.

4.2 Recall & Precision

Table 7 reports the results of detected data races by ECHO. Each race has a unique signature, *i.e.*, a pair of program statements. For several benchmarks, they have one or more known data races. We first studied all the known races and manually inspected the races reported by ECHO. We found that ECHO reported all those known races in these benchmarks. ECHO detected 23 true races and 13 false positives in total (all these races are available at [13]). The precision is 64% (23/36) and recall 100%. Nevertheless, ECHO may still miss certain true races in other JAVA programs because of its limited support of language features (*e.g.*, ECHO does not handle reflection) and its use of may-alias (as described in Section 3.3). However, both of these two issues are fundamental to static analysis.

False Positives. ECHO reported 13 false positives in these benchmarks. The false positive ratio is 36% (13/36). Previous research [12, 1] has shown numerous sources of false positives raised by static analyses. We identified three main

```

T1
class DataStorage putData():
35: if(m_iCount>=m_iSize) {
    doubleArray();
}
38: m_DataArray[m_iCount++] = new Data(data);

T2
class DataPrinter run():
18: int rand = Math.abs(randomizer.nextInt());
19: int count = DataStorage.getInstance().getCount();
20: int place=rand%count;
23: DataStorage.getInstance().getData(place);

class DataStorage getCount():
43: synchronized (m_sync) {    31: return m_iCount;
45:     ret = m_DataArray[place]
}

```

Figure 6: False positives on “m_DataArray[x]” in FileWriter due to indistinguishable array indexing.

sources in our experiments:

Object Sensitivity. Identifying static objects by their allocation sites is imprecise. It can often lead to false positives between accesses to objects allocated at the same program location but are different. Consider an example in *WebLech* (Figure 5). The program starts two concurrent threads, both executing the `run` method of the `Spider` class. Each thread creates a new `URLGetter` object at line 163 and uses it to fetch URL, which accesses the object field `failureCount` at lines 63, 67, 132. ECHO reported 6 false positives on `failureCount` because the underlying pointer analysis does not distinguish the two `URLGetter` objects allocated at the same site by the two threads.

Indistinguishable Array Indexing. ECHO does not distinguish between different array indexes, which can lead to false alarms because accesses to different elements of the same array are considered as to the same memory. This issue can be more complicated when the array indexes are under complex data flow and path conditions. Consider an example in *FileWriter* (Figure 6). Thread T_1 writes to a shared array `m_DataArray` with index `m_iCount` at line 38. Thread T_2 reads `m_DataArray` with index `place`, which is computed by a *random* value *mod* `m_iCount`. The two indexes cannot be equal because of the *mod* operation. ECHO reported 2 false positives in *FileWriter* due to this problem.

Ad Hoc Synchronization. ECHO handles standard thread synchronizations in JAVA such as thread `fork`, `join` and the `synchronized` keyword but does not recognize ad hoc synchronizations. Missing ad hoc synchronizations caused several false positives in our experiments. Due to space reasons we refer the readers to our technical report [27] for a detailed example of this issue.

These issues open several interesting directions that we plan to investigate in future work. For example, adding more object and context sensitivity [28, 29, 16] in the points-to analysis could reduce false positives. A more precise array index analysis [30, 31, 32] could alleviate the second issue, and recognizing ad hoc synchronizations [33, 34, 35] could further improve the precision of ECHO.

5. RELATED WORK

A large number of static race detection techniques have been proposed, including many types systems [36, 37, 38,

39, 40, 41, 42], scalable whole-program analyses [1, 2, 43, 44, 12], model checking [45, 46], and other specialized techniques [47, 48]. The key advantage of static race detection is that it provides the potential to detect all races over all program paths, which eliminates false negatives, although most techniques in practice sacrifice soundness for scalability.

A primary limitation of static analysis is that it is imprecise and may produce false positives. A few sophisticated data flow analyses [12, 43, 44] have been proposed to improve precision via more expensive analysis. Compared to existing techniques, ECHO amortizes the analysis cost across many small program changes and avoids redundant computation through change-aware analysis. Moreover, ECHO works in the IDE and can detect races and warn the developers to fix the races as they are introduced.

Type-based race checking systems [39, 42, 36, 37, 38] can perform well in the IDE, but they typically require a significant amount of manual annotations and/or work only for an ideal language. ECHO is fully automatic without any annotation and works for a realistic Java-like language.

Praun *et al.* [49] propose an object use graph (OUG) model that statically approximates the happens-before relation between accesses to a specific object. The key differences between OUG and our SHB graph are that the SHB graph is field-sensitive while OUG is object-sensitive and that OUG does not model lock operations.

Points-to analysis has been extensively researched [14, 28, 16, 29, 21, 15] in several different dimensions, *e.g.*, flow-sensitivity, context-sensitivity, heap modeling, etc. Precise points-to analysis is NP-hard [15]. Any practical points-to analysis must approximate the exact solution and balance between precision and performance.

A few incremental and demand-driven points-to analysis algorithms have been proposed, based on CFL reachability [50, 17], logic programming [51], and data flow analysis [52, 18]. However, demand-driven approaches do not handle changes, and existing incremental approaches cannot efficiently handle code deletion. Moreover, none of them has been applied in IDEs for multithreaded programs before.

6. CONCLUSION AND FUTURE WORK

We have presented a new IDE-based static race detection technique and a tool, ECHO, that can detect data races as soon as they are introduced into the program. ECHO is powered by a set of novel change-aware static analyses that efficiently compute change-relevant program information upon code changes without re-analyzing the whole program. Our results on a variety of multithreaded benchmarks and real-world JAVA applications show that ECHO can detect races within milliseconds upon a code change with a reasonable precision. In future work, we plan to conduct empirical studies with developers to evaluate the usability and usefulness of ECHO for diagnosing and fixing real races. We also plan to improve the precision of ECHO by addressing the sources of false positives as discussed in Section 4.2.

7. ACKNOWLEDGEMENTS

The authors would like to thank Lawrence Rauchwerger and the anonymous reviewers for their constructive comments on earlier versions of this paper. This research is supported by faculty start-up funds from Texas A&M University, a Google Faculty Award, and NSF award CCF-1552935.

8. REFERENCES

- [1] Mayur Naik, Alex Aiken, and John Whaley. Effective static race detection for Java. In *ACM SIGPLAN Conference on Programming Language Design and Implementation*, pages 308–319, 2006.
- [2] Jan Wen Vong, Ranjit Jhala, and Sorin Lerner. Relay: static race detection on millions of lines of code. ESEC-Joint European Software Engineering Conference and ACM SIGSOFT Symposium on Foundations of Software Engineering, 2007.
- [3] Cormac Flanagan and Stephen N. Freund. FastTrack: efficient and precise dynamic race detection. In *ACM SIGPLAN Conference on Programming Language Design and Implementation*, pages 121–133, 2009.
- [4] Jeff Huang, Patrick O’Neil Meredith, and Grigore Rosu. Maximal sound predictive race detection with control flow abstraction. In *ACM SIGPLAN Conference on Programming Language Design and Implementation*, pages 337–348, 2014.
- [5] Swarnendu Biswas, Minjia Zhang, Michael D. Bond, and Brandon Lucia. Valor: Efficient, software-only region conflict exceptions. In *ACM SIGPLAN Conference on Object Oriented Programming, Systems, Languages, and Applications*, 2015.
- [6] Veselin Raychev, Martin Vechev, and Manu Sridharan. Effective race detection for event-driven programs. In *ACM SIGPLAN International Conference on Object Oriented Programming Systems Languages and Applications*, pages 151–166, 2013.
- [7] ThreadSanitizer Documentation. <http://clang.llvm.org/docs/ThreadSanitizer.html>.
- [8] Intel inspector. <https://software.intel.com/en-us/intel-inspector-xe/>.
- [9] Go data race detector. https://golang.org/doc/articles/race_detector.html.
- [10] The True Cost of a Software Bug. <http://blog.celerity.com/the-true-cost-of-a-software-bug/>.
- [11] The Eclipse IDE. <https://eclipse.org/downloads/>.
- [12] Dawson Engler and Ken Ashcraft. Racerx: effective, static detection of race conditions and deadlocks. In *ACM Symposium on Operating Systems Principles*, 2003.
- [13] The ECHO webpage. <http://parasol.tamu.edu/~jeff/echo/>.
- [14] Yannis Smaragdakis and George Balatsouras. Pointer analysis. *Found. Trends Program. Lang.*, 2(1):1–69, 2015.
- [15] William Landi and Barbara G. Ryder. Pointer-induced aliasing: A problem classification. In *Proceedings of the 18th ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages*, pages 93–103, 1991.
- [16] John Whaley and Monica S. Lam. Cloning-based context-sensitive pointer alias analysis using binary decision diagrams. In *Proceedings of the ACM SIGPLAN 2004 Conference on Programming Language Design and Implementation*, pages 131–144, 2004.
- [17] Yi Lu, Lei Shang, Xinwei Xie, and Jingling Xue. An incremental points-to analysis with cfl-reachability. In *Compiler Construction*, pages 61–81, 2013.
- [18] Jyh-shiarn Yur, Barbara G Ryder, and William A Landi. An incremental flow-and context-sensitive pointer aliasing analysis. In *Proceedings of the 21st international conference on Software engineering*, pages 442–451, 1999.
- [19] Robert O’Callahan and Jong-Deok Choi. Hybrid dynamic data race detection. In *ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming*, 2003.
- [20] WALA pointer analysis. <http://wala.sourceforge.net/wiki/\index.php/UserGuide:PointerAnalysis>.
- [21] Ben Hardekopf and Calvin Lin. The ant and the grasshopper: Fast and accurate pointer analysis for millions of lines of code. In *Proceedings of the 28th ACM SIGPLAN Conference on Programming Language Design and Implementation*, pages 290–299, 2007.
- [22] Cormac Flanagan and Shaz Qadeer. A type and effect system for atomicity. In *ACM SIGPLAN Conference on Programming Language Design and Implementation*, 2003.
- [23] T. J. Watson Libraries for Analysis (WALA). <http://wala.sourceforge.net/>.
- [24] Jeff Huang. Stateless model checking concurrent programs with maximal causality reduction. In *ACM SIGPLAN Conference on Programming Language Design and Implementation*, pages 165–174, 2015.
- [25] H2 Database Engine. <http://www.h2database.com>.
- [26] WebLech URL Spider. <http://weblech.sourceforge.net>.
- [27] ECHO technical report. <http://parasol.tamu.edu/~jeff/academic/echo-tr.pdf>.
- [28] George Kastrinis and Yannis Smaragdakis. Hybrid context-sensitivity for points-to analysis. In *Proceedings of the 34th ACM SIGPLAN Conference on Programming Language Design and Implementation*, PLDI ’13, pages 423–434, 2013.
- [29] Manu Sridharan and Rastislav Bodík. Refinement-based context-sensitive points-to analysis for java. In *Proceedings of the 27th ACM SIGPLAN Conference on Programming Language Design and Implementation*, PLDI ’06, pages 387–400, 2006.
- [30] Patrick Cousot, Radhia Cousot, and Francesco Logozzo. A parametric segmentation functor for fully automatic and scalable array content analysis. In *ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages*, 2011.
- [31] Denis Gopan, Thomas Reps, and Mooly Sagiv. A framework for numeric analysis of array operations. In *ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages*, 2005.
- [32] Nicolas Halbwachs and Mathias Péron. Discovering properties about arrays in simple programs. In *ACM SIGPLAN Conference on Programming Language Design and Implementation*, 2008.
- [33] Weiwei Xiong, Soyeon Park, Jiaqi Zhang, Yuanyuan Zhou, and Zhiqiang Ma. Ad hoc synchronization considered harmful. In *USENIX Symposium on Operating Systems Design and Implementation*, 2010.
- [34] Jeff Huang and Lawrence Rauchwerger. Finding schedule-sensitive branches. In *Joint European Conference and ACM SIGSOFT Symposium on Foundations of Software Engineering*, 2015.
- [35] Le Yin. Effectively recognize ad hoc synchronizations

- with static analysis. In *LCPC*, 2013.
- [36] Chandrasekhar Boyapati, Robert Lee, and Martin Rinard. Ownership types for safe programming: preventing data races and deadlocks. In *ACM SIGPLAN Conference on Object Oriented Programming, Systems, Languages, and Applications*, 2002.
- [37] Nicholas D. Matsakis and Thomas R. Gross. A time-aware type system for data-race protection and guaranteed initialization. In *ACM SIGPLAN Conference on Object Oriented Programming, Systems, Languages, and Applications*, 2010.
- [38] Chandrasekhar Boyapati and Martin Rinard. A parameterized type system for race-free Java programs. In *ACM SIGPLAN Conference on Object Oriented Programming, Systems, Languages, and Applications*, 2001.
- [39] Cormac Flanagan and Stephen N. Freund. Type-based race detection for Java. In *ACM SIGPLAN Conference on Programming Language Design and Implementation*, 2000.
- [40] Martin Abadi, Cormac Flanagan, and Stephen N. Freund. Types for safe locking: Static race detection for Java. *ACM Trans. Program. Lang. Syst.*, 28(2):207–255, 2006.
- [41] Bart Jacobs, Frank Piessens, Jan Smans, K. Rustan M. Leino, and Wolfram Schulte. A programming model for concurrent object-oriented programs. *ACM Trans. Program. Lang. Syst.*, 31(1):1:1–1:48, 2008.
- [42] Cormac Flanagan, K. Rustan M. Leino, Mark Lillibridge, Greg Nelson, James B. Saxe, and Raymie Stata. Extended static checking for Java. In *ACM SIGPLAN Conference on Programming Language Design and Implementation*, 2002.
- [43] Mayur Naik and Alex Aiken. Conditional must not aliasing for static race detection. In *ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages*, 2007.
- [44] Polyvios Pratikakis, Jeffrey S. Foster, and Michael Hicks. Locksmith: context-sensitive correlation analysis for race detection. In *ACM SIGPLAN Conference on Programming Language Design and Implementation*, 2006.
- [45] Thomas A. Henzinger, Ranjit Jhala, and Rupak Majumdar. Race checking by context inference. In *ACM SIGPLAN Conference on Programming Language Design and Implementation*, 2004.
- [46] Vineet Kahlon, Yu Yang, Sriram Sankaranarayanan, and Aarti Gupta. Fast and accurate static data-race detection for concurrent programs. In *International Conference on Computer Aided Verification*, 2007.
- [47] Cosmin Radoi and Danny Dig. Practical static race detection for Java parallel loops. In *Proceedings of the 2013 International Symposium on Software Testing and Analysis*, pages 178–190, 2013.
- [48] Vineet Kahlon, Nishant Sinha, Erik Kruus, and Yun Zhang. Static data race detection for concurrent programs with asynchronous calls. In *Joint European Software Engineering Conference and ACM SIGSOFT Symposium on Foundations of Software Engineering*, 2009.
- [49] Christoph von Praun and Thomas R. Gross. Static conflict analysis for multi-threaded object-oriented programs. In *ACM SIGPLAN Conference on Programming Language Design and Implementation*, 2003.
- [50] Manu Sridharan, Denis Gopan, Lexin Shan, and Rastislav Bodík. Demand-driven points-to analysis for java. In *Proceedings of the 20th Annual ACM SIGPLAN Conference on Object-oriented Programming, Systems, Languages, and Applications*, pages 59–76, 2005.
- [51] Diptikalyan Saha and C. R. Ramakrishnan. Incremental and demand-driven points-to analysis using logic programming. In *Proceedings of the 7th ACM SIGPLAN International Conference on Principles and Practice of Declarative Programming*, pages 117–128, 2005.
- [52] Steven Arzt and Eric Bodden. Reviser: Efficiently updating ide-/ifds-based data-flow analyses in response to incremental program changes. In *Proceedings of the 36th International Conference on Software Engineering*, pages 288–298, 2014.